C Programming Tools: Part 1

Building and Using your own Toolkit

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 - Learn how to write portable code.



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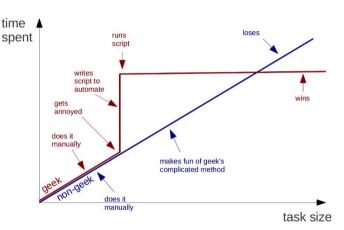
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- Doing something boring and repetitive? Can I save time by automating this?

Or, to put that another way: (As seen on the walkway last year).

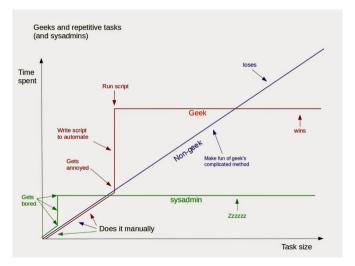


Or, to put that another way (thanks due to SwissMiss):

Geeks and repetitive tasks



Or, adding SysAdmins into the mix:



- Programmer's Editors: Use a single editor well.
- Automating Compilation: Use make.
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- There's a tarball of examples associated with each lecture, as a shorthand tarball 01.intlist refers to the directory called **01.intlist** inside the tarball. Each directory contains a README file

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- Hunt & Thomas write (in Tip 22):
 - Use a Single Editor Well: The editor should be an extension of your hand; make sure your editor is configurable, extensible and programmable.
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- Coding might be 80% thinking and 20% typing, but your typing must not interfere with your thought process.

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- Why? Because programmers are notoriously sectarian when it comes to...



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- Note that Hunt & Thomas aren't much in favour of IDEs. Neither am I:-)

Actually, it's well known that Real Programmers use Butterflies to edit source code:

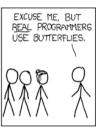




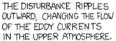
















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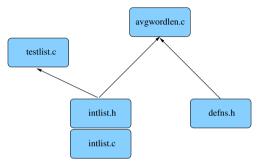
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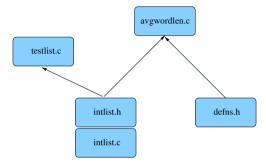


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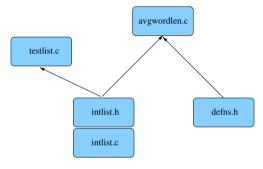


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- Module intlist comprising two files (interface intlist.h and implementation intlist.c) defining a list-of-integers type.
- Separate basic definitions header file defns.h.
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So, what should we compile? what should we link?



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• Which gives:

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intlist.c:#include "intlist.h"
avgwordlen.c:#include "intlist.h"
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- Make uses such file dependencies, encoded in a Makefile, to automatically compile your programs.
- The Makefile contains dependency rules between target and source files with optional actions (commands) to generate each target from the corresponding sources.

CC= gcc

CFLAGS = -Wall

= testlist avgwordlen BUILD

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all: \$(BUILD)

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/bin/rm -f \$(BUILD) *.o core

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• Makefiles also contain macros, eg \$(CC) which C compiler to use, \$(CFLAGS) what C compiler flags etc. Environment variables become macros too, eg \$(HOME).

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- Makefiles also contain macros, eg \$(CC) which C compiler to use, \$(CFLAGS) what C compiler flags etc. Environment variables become macros too, eg \$(HOME).
- Note that Make needs very few explicit dependencies and even fewer explicit actions, because it already knows that intlist.o depends on intlist.c, and how to compile .c files.

 This rule declares that intlist.o is up to date only if it is newer than intlist.c and intlist.h. If it doesn't exist or is older than either file, then the action is triggered compiling intlist.c.

```
intlist.o:
                   intlist.c intlist.h
        $(CC) $(CFLAGS) -c intlist.c
```

- This rule declares that intlist o is up to date only if it is newer than intlist c and intlist.h. If it doesn't exist or is older than either file, then the action is triggered compiling intlist.c.
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- For example, if intlist.h is altered, you run make, that builds the target all, which recursively applies all the rules checking timestamps and concludes that...
- ...intlist.c, testlist.c and avgwordlen.c need recompiling, and then the new testlist.o and avgwordlen.o need relinking against the new intlist.o, giving the 2 executables testlist and avgwordlen.

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- Summary: Always use make, or some similar tool. Keep your Makefile dependencies up to date, optionally auto-generating your Makefiles.
- Google make tutorial for more info.

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- Core concept: each sub-directory contains:
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- What to split? The intlist module is:
 - Logically separate.
 - Reusable whenever we want a list of integers.
 - Depends on only the standard library.
- That is, it's highly cohesive.
- So: it's perfect for splitting out into a library sub-directory.
- In tarball directory 04.intlist-with-lib, you'll see what we have done to achieve this.

- There's a separate lib sub-directory, let's explore it first:
- lib contains intlist.c, intlist.h, testlist.c and it's own Makefile, lib/Makefile, which builds two core targets:
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• The new rule says that \$(LIB) depends on \$(LIBOBJS), i.e. libintlist.a depends on intlist.o, and that the action invokes ar and ranlib - tools that build library files.

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cd lib; make
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• This new always run rule tricks Make, with it's single directory view of the world, into first building in the lib sub-directory, before building in the current directory.



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- Left for you to work through!

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- Go through that, and you'll get a taste of how CMake lists files are constructed. But CMake is over complex for my tastes. Also, any tool that needs to be run in it's own build subdirectory in order to leave the source code directory uncluttered is too messy for me!